LISTING OF THE CLAIMS

1. (Currently Amended) An active matrix display device including a display panel, a common electrode, and a source electrode, in which the common electrode and the source electrode are disposed so as to sandwich the display panel therebetween, and having a plurality of display modes, the active matrix display device comprising:

storage means for storing values of optimum voltages so as to match a center of a voltage waveform of a voltage applied to the common electrode with a center of a voltage waveform of a voltage applied to the source electrode in each of the display modes, each of which optimum voltage ~~values~~ is applied to an electrode having a shiftable voltage waveform; and

voltage applying means for reading out the optimum voltage value corresponding to the display mode from the storage means and applying the read optimum voltage ~~value~~ to shift the voltage waveform of the electrode.

2. (Previously Presented) The active matrix display device according to claim 1, wherein:

the storage means, which are connected to a common electrode drive circuit, are for storing a plurality of voltage values for shifting a voltage waveform of a voltage applied to the common electrode, respectively for the display modes.

3. (Previously Presented) The active matrix display device according to claim 1, wherein:

the storage means, which are connected to a source electrode drive circuit, are for storing a plurality of voltage values for shifting a voltage waveform of a voltage applied to the source electrode, respectively for the display modes.

4. (Cancelled)

5. (Cancelled)

6. (Previously Presented) The active matrix display device according to claim 1, wherein:

the optimum applied voltage value is determined in accordance with a lowest value and a voltage width of the optimum applied voltage.

7. (Previously Presented) The active matrix display device according to claim 1, wherein:

the optimum applied voltage value is determined in accordance with a highest value and a voltage width of the optimum applied voltage.

8. (Previously Presented) The active matrix display device according to claim 1, wherein:

the optimum applied voltage value is determined in accordance with a center value and a voltage width of the optimum applied voltage.

9. (Previously Presented) The active matrix display device according to claim 1, wherein:

the optimum voltage values are voltage values to shift a center of the voltage waveform without shifting an amplitude of the voltage waveform.

10. (Previously Presented) The active matrix display device according to claim 9, wherein the display panel comprises, in each pixel, a thin film transistor including:

the source electrode which receives a source voltage from a source signal line,

a gate electrode which receives a gate voltage from a gate signal line, and

a drain electrode which applies the source voltage to the pixel,

wherein matching of the centers of the voltage waveforms are to compensate a parasite capacitance between the gate electrode and the drain electrode.

11. (Previously Presented) The active matrix display device according to claim 10, wherein the display modes include a reflective mode in which external light is utilized for displays with a back light turned off and a transmissive mode in which a back light is utilized for displays.